

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
40V	6.2mΩ@10V	50A
	8.4mΩ@4.5V	

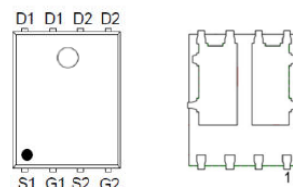
Feature

- Split Gate Trench Technology
- Low $R_{DS(on)}$
- Low Gate Charge
- Low Gate Resistance
- 100% UIS Tested

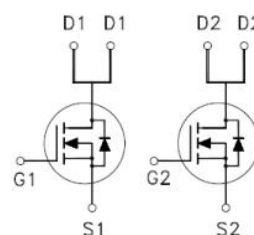
Application

- Power Management
- Load Switching

PDFN5X6-8L



Schematic diagram



Package Marking and Ordering Information

Part Number	Package	Marking	Packing	Reel Size	Tape Width	Qty
GPT050ND04LND	PDFN5X6-8L	T050ND04N	Reel & Tape	330mm	12mm	5000pcs

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter		Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	40	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current ¹	$T_C = 25^\circ\text{C}$	I_D	50	A
	$T_C = 100^\circ\text{C}$		32	
Pulsed Drain Current ²		I_{DM}	200	A
Single Pulsed Avalanche Current ³		I_{AS}	18	A
Single Pulsed Avalanche Energy ³		E_{AS}	81	mJ
Power Dissipation ⁵		P_D	28	W
$T_C = 25^\circ\text{C}$				
Thermal Resistance from Junction to Ambient ⁶		$R_{\theta JA}$	60	$^\circ\text{C/W}$
Thermal Resistance from Junction to Case		$R_{\theta JC}$	4.5	$^\circ\text{C/W}$
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55~ +150	$^\circ\text{C}$

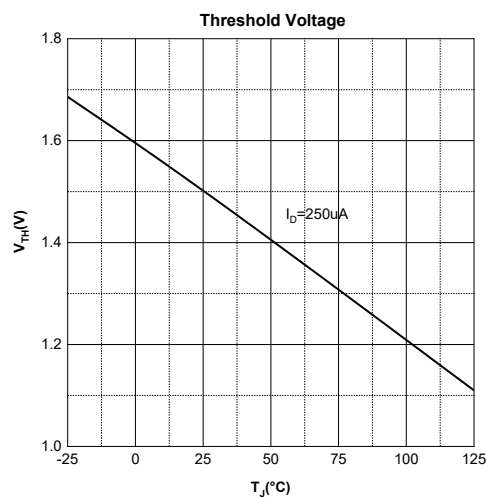
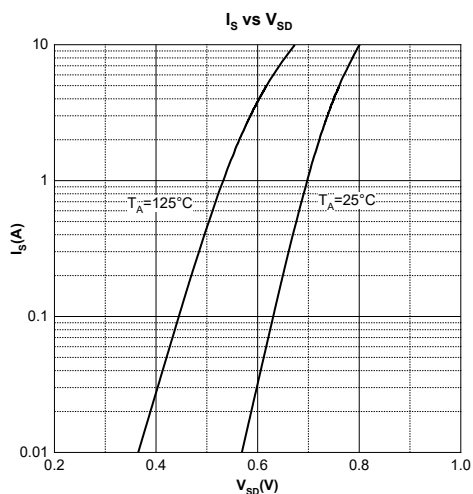
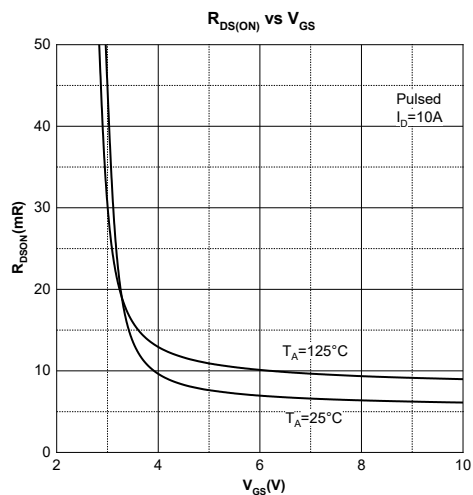
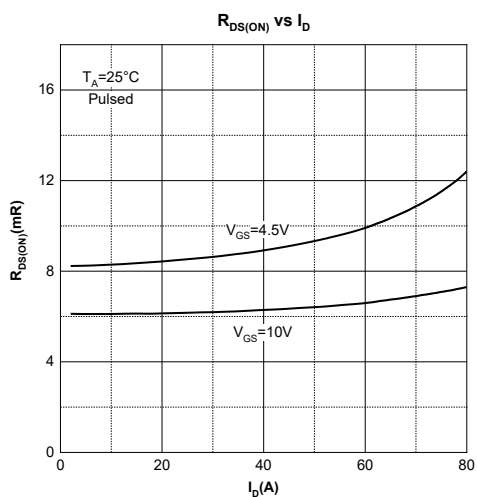
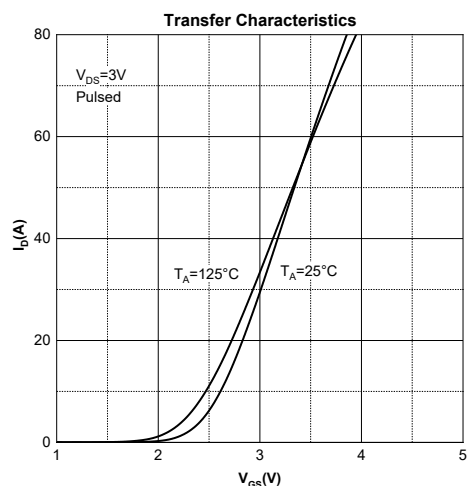
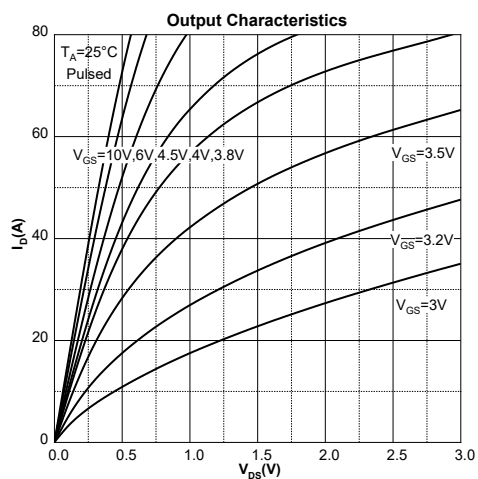
MOSFET ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

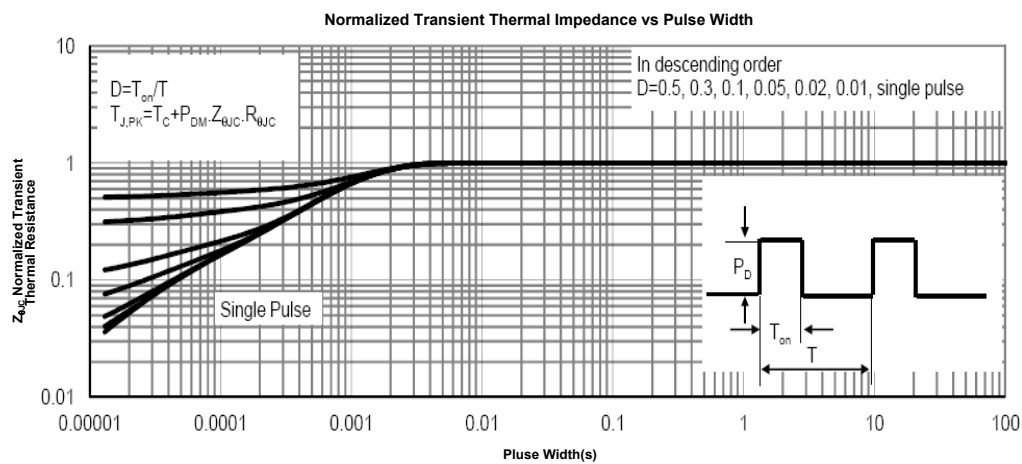
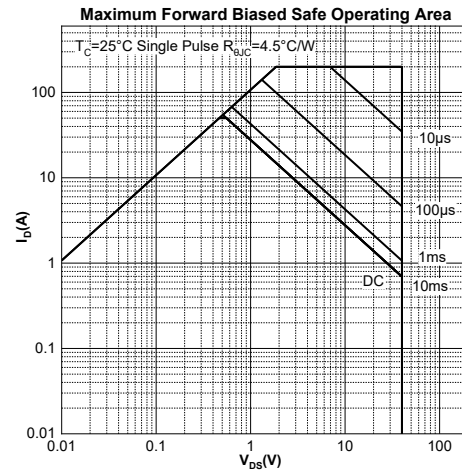
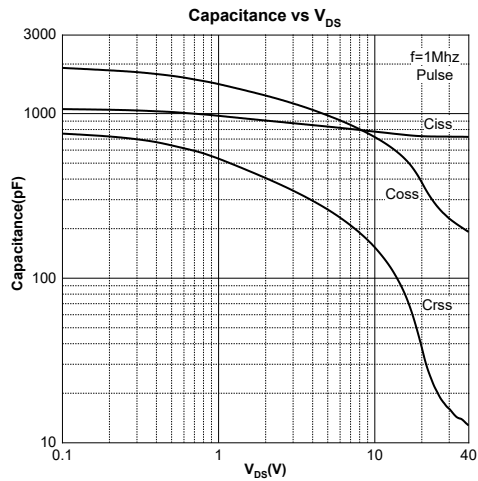
Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	40			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 40V, V _{GS} = 0V			1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} = ±20V, V _{DS} = 0V			±100	nA
On Characteristics ⁴						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	1	1.6	3	V
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} = 10V, I _D = 10A		6.2	8	mΩ
		V _{GS} = 4.5V, I _D = 10A		8.4	11	
Dynamic Characteristics						
Input Capacitance	C _{iSS}	V _{DS} = 20V, V _{GS} = 0V, f = 1MHz		732		pF
Output Capacitance	C _{oSS}			370		
Reverse Transfer Capacitance	C _{rSS}			34		
Gate Resistance	R _g	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz		3		Ω
Switching Characteristics						
Total Gate Charge	Q _g	V _{DS} = 20V, V _{GS} = 10V, I _D = 10A		18		nC
Gate-Source Charge	Q _{gs}			2.1		
Gate-Drain Charge	Q _{gd}			5.6		
Turn-On Delay Time	t _{d(on)}	V _{DD} = 20V, V _{GS} = 10V, R _G = 1.6Ω, I _D = 20A		10		ns
Turn-On Rise Time	t _r			32		
Turn-Off Delay Time	t _{d(off)}			27		
Turn-Off Fall Time	t _f			12		
Source-Drain Diode Characteristics						
Diode Forward Voltage ⁴	V _{SD}	V _{GS} = 0V, I _S = 10A			1.2	V

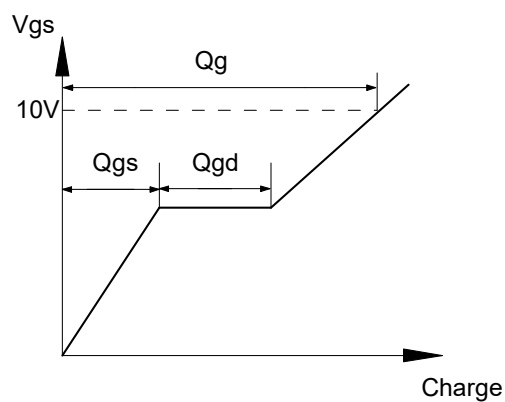
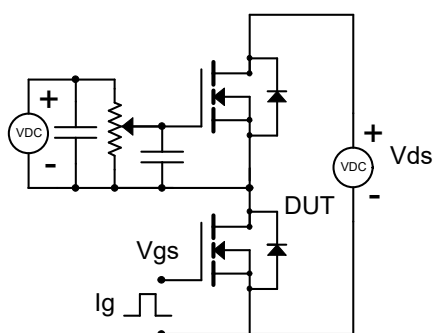
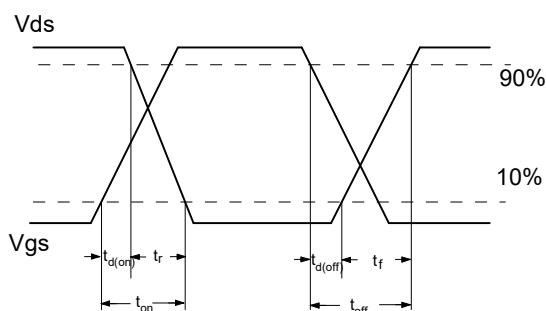
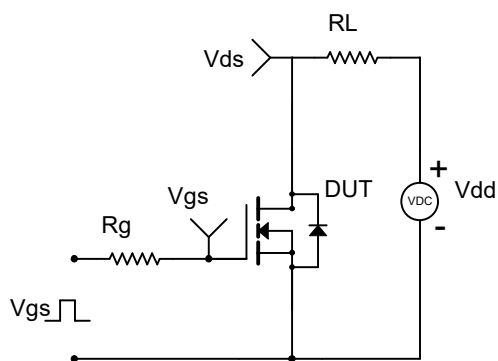
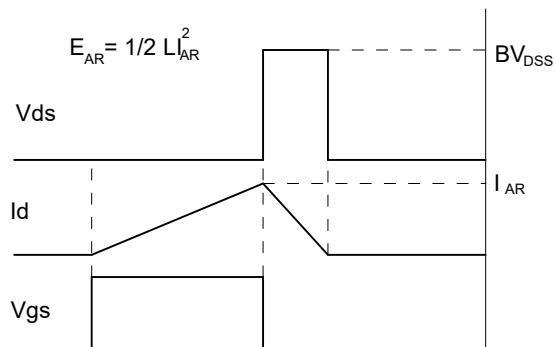
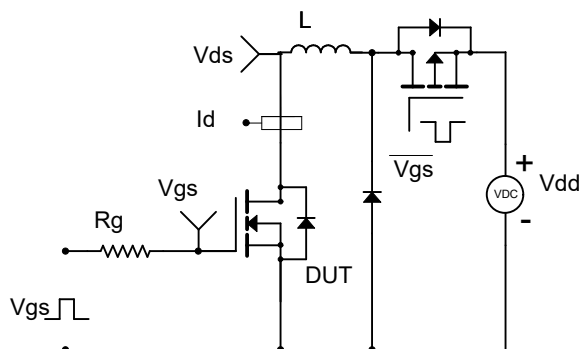
Notes:

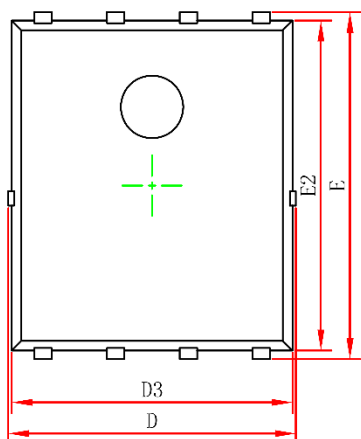
- 1.The maximum current rating is limited by package. And device mounted on a large heatsink.
- 2.Pulse Test: Pulse Width ≤ 10μs, duty cycle ≤ 1%.
- 3.EAS condition: V_{DD} = 20V, V_{GS} = 10V, L = 0.5mH, R_G = 25Ω Starting T_J = 25°C.
- 4.Pulse Test: Pulse Width ≤ 300μs, duty cycle ≤ 2%.
- 5.The power dissipation P_D is limited by T_{J(MAX)} = 150°C. And device mounted on a large heatsink.
- 6.Device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with T_A = 25°C.

Typical Characteristics

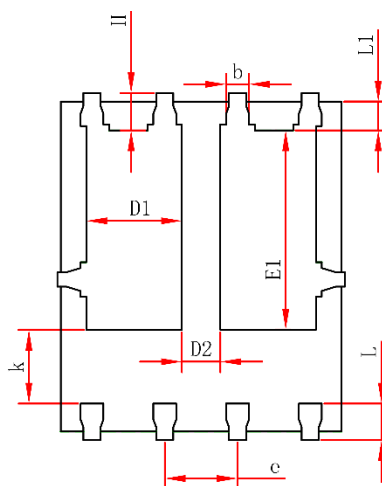




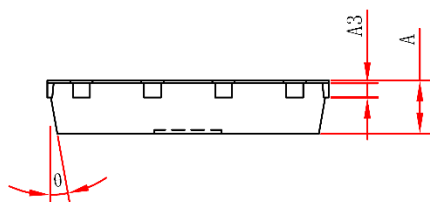
Gate Charge Test Circuit & Waveform

Resistive Switching Test Circuit & Waveform

Unclamped Inductive Switching (UIS) Test Circuit & Waveforms




Top View
[顶视图]



Bottom View
[背视图]



Side View
[侧视图]

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.100	0.035	0.043
A3	0.210	0.340	0.008	0.013
D	4.900	5.100	0.193	0.201
E	5.900	6.100	0.232	0.240
D1	1.470	1.870	0.058	0.074
D2	0.470	0.870	0.019	0.034
E1	3.340	3.640	0.131	0.143
D3	4.800	5.000	0.189	0.197
E2	5.674	5.826	0.223	0.229
k	1.100	-	0.043	-
b	0.300	0.500	0.012	0.020
e	1.270TYB		0.050TYB	
L	0.510	0.710	0.020	0.028
L1	0.424	0.576	0.017	0.023
H	0.549	0.726	0.022	0.029
θ	8°	12°	8°	12°

Attention:

- GreenPower Electronics reserves the right to improve product design function and reliability without notice.
- Any and all semiconductor products have certain probability to fail or malfunction, which may result in personal injury, death or property damage. Customer are solely responsible for providing adequate safe measures when design their systems.
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